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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/673,434

09/30/2003

Eizi Yokoyama

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EXAMINER

PHAN, THIEM D

ART UNIT

PAPER NUMBER

3729

MAIL DATE

DELIVERY MODE

07/11/2007

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/673,434

Applicant(s)

YOKOYAMA ET AL.

Examiner

Tim Phan

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 13 April 2007.
2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 4,5 and 7 is/are pending in the application.
4a) Of the above claim(s) _____ is/are withdrawn from consideration.
5) ☐ Claim(s) _____ is/are allowed.
6) ☒ Claim(s) 4,5,7 is/are rejected.
7) ☐ Claim(s) _____ is/are objected to.
8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date _____.
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____

DETAILED ACTION

1. The amendment filed on 4/13/07 has been fully considered and made of record.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all

obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 4, 5 and 7 are rejected under 35 U.S.C. 103(a) as being anticipated by Shoji et al (US 5,982,629) in view of Mita et al (US 6,297,142).

With regard to claim 4, Shojiet et al teach a method of fabricating a circuit board (Col. 3, lines 29+), comprising the steps of:

- a step of forming a terminal portion (Fig. 2, 2) in manufacturing a circuit board (Fig. 2, 1), said step of forming a terminal portion being to stack a base layer of copper (Fig. 2, 2) and a plated layer of gold (Fig. 2, 3; col. 2, lines 45 & 46) successively to form the terminal portion; except for having the circuit board as a square board made of glass epoxy resin.

Mita et al teach a method for fabricating a semiconductor device including an insulating, square board of glass epoxy resin substrate (Fig. 3A, 4; col. 21, lines 32-35), in order to improve the reliability with temperature cycling in lead-patterning substrates and semiconductor devices (Col. 4, lines 43-46).

It would be obvious to one of ordinary skill in the art at the time the invention was made to modify the method of Shojiet et al by applying the technique for fabricating a semiconductor device including an insulating, square board of glass epoxy resin substrate, as taught by Mita et al, in order to improve the reliability with temperature cycling in lead-patterning substrates and semiconductor devices.

With regard to claim 5, Shojiet al teach a further step of:

- forming an insulating layer (Fig. 9, 5) after said step of forming a terminal portion in manufacturing said circuit board, said step of forming an insulating layer being to form an insulating layer in the other area (Fig. 9, 5) than the area where said terminal portion is formed, wherein said insulating layer is formed so as to cover a peripheral edge of said plated layer (Fig. 9, 3) so that the surface of said circuit board (Fig. 9, 1) and at least one of the surface of the base layer (Fig. 9, 2) are not exposed externally.

With regard to claim 7, Shojiet al teach the further steps of:

- forming a base layer of a copper pattern (Fig. 2, 2) on a surface of an insulating board

(Fig. 2, 1);

- forming a plated layer (Fig. 2, 3) so as to cover the entire base layer (Fig. 2, 2) by selective plating (Col. 5, line 5); and
- forming an insulating layer (Fig. 9, 5) on said plated layer (Fig. 9, 3) and patterning said insulating layer so that only a portion of said plated layer is exposed externally.

Response to Arguments

4. Applicants' arguments with respect to claims 4, 5 and 7 have been considered but are moot in view of the new grounds of rejection.

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicants' disclosure.

Applicants' amendment necessitated the new grounds of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicants are reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.


6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tim Phan whose telephone number is 571-272-4568. The examiner can normally be reached on M & Tu, 6AM - 2PM, and W & Th, 9AM – 5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Peter Vo can be reached on 571-272-4690. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR

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system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



A. DEXTER TUGBANG
PRIMARY EXAMINER

Tim Phan
Examiner
Art Unit 3729

tp
June 27, 2007